



08/715869

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Reissue Application of:

Steve W. Heppler

U.S. Patent No.: 5,348,164

Issued: September 20, 1994

For: METHOD AND APPARATUS FOR
TESTING INTEGRATED CIRCUITS

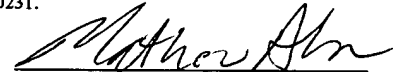
Reissue Serial No.: Unknown

Filed: Unknown

Attorney Docket No.: 2972US(92-0476-RE)

CERTIFICATE OF MAILING UNDER RULE 1.10

I hereby certify that this correspondence along with any attachments referred to or identified as being attached or enclosed is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service (under 37 C.F.R. § 1.10) (Express Mail Mailing Label Number: EM413698105US) on the date of deposit shown below with sufficient postage and in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.C. 20231.

9/19/96
Date of Deposit
Signature of person actually making deposit
pursuant to 37 C.F.R. § 1.10(b)MATHEW ALLENTyped/printed name of person whose
signature is contained aboveASSENT OF ASSIGNEE TO REISSUEAssistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The undersigned assignee of the entire interest in U.S. Patent No. 5,348,164 hereby assents to the accompanying application for reissue of U.S. Patent No. 5,348,164.

Pursuant to 37 C.F.R. § 3.73, the undersigned representative of the Assignee has reviewed the evidentiary documents, specifically the Assignment to Micron Semiconductor, Inc. recorded on September 12, 1994, at Reel 7125, Frames 0785-0788, and a Notice of Merger of Micron Semiconductor, Inc. into Micron Technology, Inc. filed concurrently herewith, and the undersigned representative certifies that to the best of his knowledge and belief, title remains in the name of Micron Technology, Inc.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the Assignee, and to take the action set forth herein on behalf of the Assignee, pursuant to a resolution of its Board of Directors.

Micron Technology, Inc.

Date: SEP 17, 1996By: Michael L. Lynch
Reg. No. 30,871
Chief Patent Counsel



08/715869

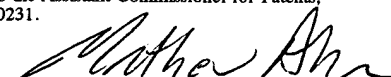
PATENT**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE****In re Reissue Application of:**

Steve W. Heppler

U.S. Patent No.: 5,348,164**Issued:** September 20, 1994**For:** METHOD AND APPARATUS FOR
TESTING INTEGRATED CIRCUITS**Reissue Serial No.:** Unknown**Filed:** Unknown**Attorney Docket No.:** 2972US(92-0476-RE)**CERTIFICATE OF MAILING UNDER RULE 1.10**

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9/19/96
Date of Deposit


Signature of person actually making deposit
pursuant to 37 C.F.R. § 1.10(b)

MATHEW ALLEN
Typed/printed name of person whose
signature is contained above

OFFER TO SURRENDER

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

The undersigned applicant of the accompanying reissue application of Letters Patent for "METHOD AND APPARATUS FOR TESTING INTEGRATED CIRCUITS", U. S. Patent No. 5,348,164, granted September 20, 1994, of which Micron Technology, Inc. is now sole owner by assignment and by merger with Micron Semiconductor, Inc., and on whose behalf and with whose assent the accompanying application is made, hereby offers to surrender said Letters Patent.


Pursuant to 37 C.F.R. § 3.73, the undersigned representative of the Assignee has reviewed the evidentiary documents, specifically the Assignment to Micron Semiconductor, Inc. recorded on September 12, 1994, at Reel 7125, Frames 0785-0788, and a Notice of Merger of Micron Semiconductor, Inc. into Micron Technology, Inc. filed concurrently

herewith, and the undersigned representative certifies that to the best of his knowledge and belief, title remains in the name of Micron Technology, Inc.

The undersigned further avers that he is empowered to make and sign the foregoing certification on behalf of the Assignee, and to take the action set forth herein on behalf of the Assignee, pursuant to a resolution of its Board of Directors.

Micron Technology, Inc.

Date: Sept 17, 1996

By: 

Michael L. Lynch
Reg. No. 30,871
Chief Patent Counsel

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**PATENT****IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Reissue Application of:

Steve W. Heppler

U.S. Patent No.: 5,348,164

Issued: September 20, 1994

For: METHOD AND APPARATUS FOR
TESTING INTEGRATED CIRCUITS

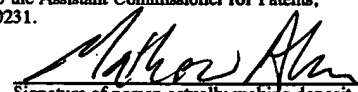
Reissue Serial No.:

Attorney Docket No.: 2972US(92-0476-RE)

CERTIFICATE OF MAILING UNDER RULE 1.10

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September 19, 1996
Date of Deposit


Signature of person actually making deposit
pursuant to 37 C.F.R. § 1.10(b)

Mathew Allen
Typed/printed name of person whose
signature is contained above

REQUEST FOR TITLE REPORT

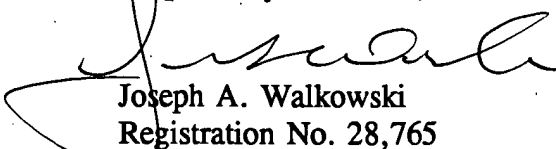
Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Please prepare a title report for U.S. Patent No. 5,348,164, issued September 20, 1994, for placing in the official file of the above-identified application for reissue of the original patent. An Assignment placing title in Micron Semiconductor, Inc. was recorded on September 12, 1994 at Reel 7125, Frames 0785-0788 and a Notice of Merger of Micron Semiconductor, Inc. into Micron Technology, Inc. is filed concurrently herewith.

A check transmitted with this reissue application is for an amount including the required fee of \$25.00. If this check is inadvertently not enclosed or is in error in amount, please charge any deficiency or credit any overpayment to Trask, Britt & Rossa Deposit Account No. 20-1469.

Respectfully submitted,


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Telephone: (801) 532-1922

Date: September 19, 1996